

Amendments to the Claims:

1. (Currently amended) A data signal receiver programmed with a loader comprising
a processor ~~containing~~ comprising
a signal processing block,
an initiating block initiating the loader, and
a loader control block servicing the loader based on a code initiated by
the initiating block;
a signal-receiving block;
data exchange interfaces linked to the processor;
RAM memory, ROM memory and NV-RAM memory linked to the processor;
and
a non-volatile memory linked to the processor wherein a decompressing
program of the loader and the loader in a compressed form are stored in the non-
volatile memory and after being decompressed, by the decompressing program, the
loader is stored in a section of the RAM memory, the section being declared as ~~a~~ the
ROM memory.
2. (Currently amended) The data signal receiver according to claim 1 wherein the signal
processing block is connected to ~~the~~ a data source through a GSM signal
transmitting/receiving block and/or an external interface block.
3. (Currently amended) The data signal receiver according to claim 1 wherein ~~an~~
a memory image is created from a section containing a loader's booting sequence,
a section containing a loader's jump table and a section containing a segment with
loader's static data and loader's code wherein the memory image is stored in the
non-volatile memory in a compressed form.

4. (Currently amended) The data signal receiver according to claim 3 wherein the loader's jump table contains addresses of functions common to a the decompressing program and the loader, the functions are defined in the decompressing program.
5. (Previously presented) The data signal receiver according to claim 1 wherein the loader's code after decompressing is located at a permanent address in the RAM memory.
6. (Currently amended) The data signal receiver according to claim 1 wherein the non-volatile memory is a FLASH memory.
7. (Currently amended) A method for updating software in a data signal receiver having a processor and data exchange interfaces, RAM, ROM, NV-RAM and a non-volatile ~~memory~~ memories linked to the processor, the method comprising
storing of software containing a loader in a compressed form in the non-volatile RAM memory; and
upon initiating startup procedure,
decompressing the software containing the loader, and
copying the software containing the loader to a permanent address in a section of the RAM memory, declared as ROM type memory, prior to a software linking process.
8. (Previously presented) The method for updating software according to claim 7 wherein a startup procedure of the loader is executed upon connecting the data signal receiver to a power source.
9. (Previously presented) The method for updating software according to claim 7 wherein a startup procedure of the loader is initiated at a user's request.

10. (Previously presented) The method for updating software according to claim 7 wherein a startup procedure of the loader is initiated by an external signal, transmitted to the data signal receiver.
11. (Currently amended) The method for updating software according to claim 7 wherein a memory image is created from a section containing a starting sequence of the loader, a section containing of a jump table of the loader and a section containing area with static data of the loader and its code and then the memory image is stored in the non-volatile memory in a compressed form.
12. (Previously presented) The method for updating software according to claim 7 further comprising
creating a jump table of the loader, the jump table containing addresses of functions common to the decompressing program and the loader, defined in the decompressing program.
13. (Currently amended) The method for updating software according to claim 7 further comprising
checking whether a software currently broadcasted in ~~the~~ a data signal is meant for the data signal receiver, in which the loader has been initiated after initiating an application update procedure; and
accepting the application update procedure when the program currently broadcasted in the data signal is meant for the data signal receiver, in which the loader has been initiated.
14. (New) A computer-readable memory containing a computer program that is executable by a processor to perform the method recited in claim 7.